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## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

*(Use as many sheets as necessary)*

**Complete if Known**

Application Number	09/954,698
Filing Date	September 11, 2007
First Named Inventor	William H. Thesling
Art Unit	2133
Examiner Name	Phung M. Chung
Attorney Docket Number	098417-610011

Sheet	1	of	2
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## U. S. PATENT DOCUMENTS

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**FOREIGN PATENT DOCUMENTS**

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**Examiner  
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Date  
Considered

7/21/05

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Sheet	2	of	2	Attorney Docket Number	098417-610011

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Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
me		R. Pyndiah, P. Combelles and P. Adde, "A very low complexity block turbo decoder for product codes," in proc. of IEEE 1996, pp. 101-105.	
me		A. Picart and R. Pyndiah, "Performance of Turbo-Decoded Product Codes Used in Multilevel Coding," in proc. of IEEE ICC'96 Conference, Dallas TX 1996, pp. 107-111	
me		R. Pyndiah, A. Glavieux, A. Picart and S. Jacq, "Near optimum decoding of product codes," in proc. of IEEE Globecom '94 Conference, vol. 1/3, Nov.-Dec. 1994, San Francisco, pp. 339-343.	
me		R. Pyndiah, "Near-optimum decoding of product codes: Block turbo codes, IEEE Transactions on Communications, Vol. 46, No. 8, August 1998, pp. 1003-1010	

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